

JCN70

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10092874	FILING DATE 03/05/2002	CLASS 285 736	SUBCLASS 736	CAU 285 287	EXAMINER W. Fahmy
**APPLICANTS: Hishiro Yoshiki;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no		MI22-1873	
Verified and Acknowledged Examiners's initials					
TITLE : Semiconductor constructions; and methods of forming semiconductor constructions					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		Total Claims	
Amount Due	Date Paid	Print Claim for 0.6	
		DRAWING	
		Sheets Drwg.	Print Fig.
		Primary Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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